

**AMENDMENTS TO THE SPECIFICATION**

***Please replace paragraph [44] with the following amended paragraph:***

[44] The radio signal applied to the WLAN reception processor 6 is amplified by a low noise amplifier (LNA) 128. The low noise amplifier (LNA) 128 applies the amplified signal to a band pass filter (BPF) 213 which, in turn, ~~reject~~ rejects noise components of the amplified signal. The filtered signal generated from the band pass filter (BPF) 213 is applied to a down mixer 138 which converts downward in frequency the filtered signal into an intermediate frequency signal. Then, the frequency supplied to the down mixer 138 is produced by the ~~four~~ fourth distributor 268. The converted intermediate frequency signal generated from the down mixer 138 is applied to an automatic gain controller (AGC) and demodulator 148 which amplifies the converted intermediate frequency signal to a proper level and ~~demodulate~~ demodulates the amplified signal. The demodulated signal generated from the automatic gain controller (AGC) and demodulator 148 is applied to an A/D converter 168 which converts the demodulated signal into a digital signal. The converted digital signal is applied to the medium access controller (MAC) 175 which processes the digital signal for application to the digital baseband processor 170. The digital baseband processor 170 performs a digital signal process (DSP) for the processed signal applied thereto from the medium access controller (MAC) 175 to conform to a WLL transmission scheme for application to the WLL transmission processor 5.